	Hits	Search Text	DBs	Time Stamp
1	13	(synchronization adj logic) near10 (bus adj interface)	USPAT	2004/10/28 09:55
2	2	(synchronization adj logic) near10 (bus adj interface)	US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 15:28
3	1	(synchronization adj logic) near5 peripheral	USPAT	2004/10/27 16:05
4	2	(synchronization adj logic) near5 peripheral	US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 15:35
5	0	synchroniz$3 near5 (asynchronous adj peripheral)	USPAT	2004/10/27 16:06
6	66	synchroniz$3 adj (asynchronous adj data)	USPAT	2004/10/27 16:30
7	3	synchroniz$3 adj (asynchronous adj data)	EPO; JPO; IBM_TDB	2004/10/27 16:23
8	11	(synchroniz$3 adj3 data) with (first adj clock adj domain) with (second adj clock adj domain)	USPAT	2004/10/27 18:29
9	3	(synchroniz$3 adj3 data) with (first adj clock adj domain) with (second adj clock adj domain)	US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:40
10	1	(synchronization adj performed) near3 peripheral	USPAT	2004/10/27 16:44
11	1	(synchronization adj performed) near3 peripheral	US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:43
12	0	((centralized adj synchronization) near5 data) with (different adj clock adj domains)	USPAT	2004/10/27 16:45
13	0	((centralized adj synchronization) near5 data)	USPAT	2004/10/27 16:49
14	1	((centralized adj synchronization) near5 data)	US-PGPUB; EPO; JPO; IBM_TDB	2004/10/27 16:49
15	2	("5345453" "5602878").pn.	USPAT	2004/10/27 18:08
16	8	5602878.URPN.	USPAT	2004/10/27 17:27
17	5	("5115455" | "5138189" | "5291529" | "5339395" | "5359630").PN.	USPAT	2004/10/27 17:37
18	0	(register near2 (sample adj data)) same comparator same multplexor	USPAT	2004/10/27 18:09
19	0	(synchroniz$5 adj (unit logic circuit)) with built with (bus adj (interface logic circuit$2))	USPAT	2004/10/28 10:12
20	61	(synchroniz$5 adj (unit logic circuit)) with (bus adj (interface logic circuit$2))	USPAT	2004/10/27 18:19

	Hits	Search Text	DBs	Time Stamp
21	1	(synchroniz$5 adj (unit logic circuit)) with (bus adj (interface logic circuit$2))	EPO; JPO; IBM_TDB	2004/10/27 18:20
22	669	synchronization adj logic	USPAT	2004/10/27 18:30
23	67	synchronization adj logic with (peripheral device)	USPAT	2004/10/27 18:30
24	2	synchronization adj logic with (peripheral device) with bus	USPAT	2004/10/27 18:30
25	6	("5796992" | "5912572" | "5958055" | "6075830" | "6084447" | "6567921").PN.	USPAT	2004/10/27 18:32
26	166	(sample adj data) near5 bus	USPAT	2004/10/28 10:55
27	7	((sample adj data) near5 bus) with device	USPAT	2004/10/28 09:57
28	25	(synchroniz$5 adj (unit logic circuit)) with built with (peripheral device circuit)	USPAT	2004/10/28 10:29
29	9	(synchroniz$5 adj (unit logic circuit)) with built with (peripheral device circuit)	EPO; JPO; IBM_TDB	2004/10/28 10:29
30	22	((sample adj data) near5 bus).clm.	USPAT	2004/10/28 11:01
31	0	synchronization near2 (peripheral adj bus)	USPAT	2004/10/28 11:02
32	14	synchronization with (peripheral adj bus)	USPAT	2004/10/28 11:16
33	12	synchronization with (peripheral adj bus)	US-PGPUB; EPO; JPO; IBM_TDB	2004/10/28 11:14
34	32	synchronization near5 decentralized	USPAT	2004/10/28 11:28
35	1	synchronization near5 centralized with (devices peripherals)	USPAT	2004/10/28 11:29
36	92	synchronization near5 centralized	USPAT	2004/10/28 11:33